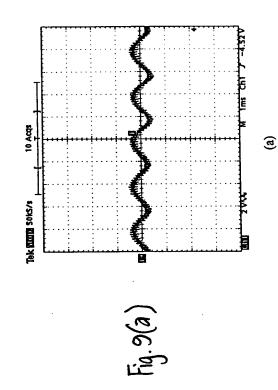
Approved by 5-30-2003 6 30 Acqs Ch3 10.0 V € BIE 10.0mV Tek (100MS/s

Fig. 8 Experimental waveforms of the SEPIC converter. Ch2: switch voltage stress, 50V/div; Ch3: input voltage, 10V/div; Ch4: input current, 0.5A/div.



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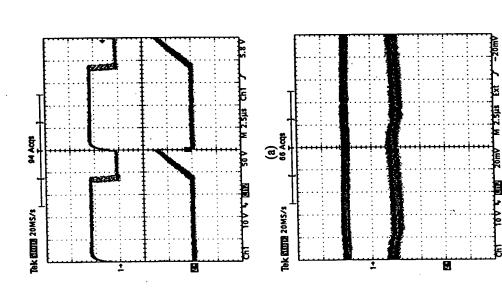
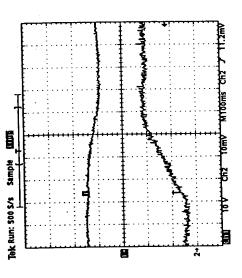


Fig. 7 Detailed experimental waveforms of the SEPIC converter. (a) Ch1: gate signal, 10V/div; Ch2: switch voltage stress, 50V/div. (b) Ch1: input voltage, 10V/div; Ch2: input current, 0.5A/div.

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Approved by B.V. 5-30-2003



from 500W to 900W. Ch1: input voltage, 10V/div. Ch2: input current, 0.5A/div. Fig. 10 Transient waveforms of the SEPIC converter subject to Plamp changed

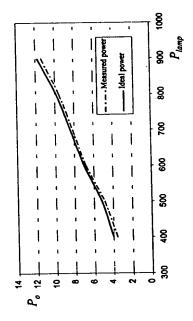


Fig. 11 Comparison of maximum solar panel output power using proposed method and the ideal ones in Fig. 6(b), under different Plamp

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16 Acqs Tek Etters Soks/s

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9 Tek etana soks/s

Fig. 9(c)

Fig. 9(b)